

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)			ATTY. DOCKET NO. 43876-155		SERIAL NO. 10/757516 Continuation of Serial No. 10/646,787		
			APPLICANT HANSEN, et al.				
			FILING DATE January 15, 2004		GROUP To be assigned		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
EC	5,721,892	02/24/98	Peleg, et al.				
	5,734,874	03/31/98	Van Hook, et al.				
	5,757,432	05/26/98	Dulong, et al.				
	5,758,176	05/26/98	Agarwal, et al.				
	5,802,336	09/01/98	Peleg, et al.				
	5,809,292	09/15/98	Wilkinson, et al.				
	5,818,739	10/06/98	Peleg, et al.				
	5,825,677	10/20/98	Agarwal, et al.				
	5,835,782	11/10/98	Chu Lin, et al.				
	5,886,732	03/23/99	Humpleman				
	5,922,066	07/13/99	Cho, et al.				
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	6,016,538	01/18/00	Guttag, et al.				
	6,092,094	07/18/00	Ireton				
	EC	6,401,194 B1	06/04/02	Nguyen, et al.			
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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER ART (including Author, Title, Date, Pertinent Pages, etc.)							
EXAMINER <i>Eric</i>				DATE CONSIDERED 2/3/09			

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EC	5,408,581	04/18/95	Suzuki, et al.			
	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
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	5,557,724	09/17/96	Sampat, et al.			
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	5,640,543	06/17/97	Farrell, et al.			
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EC	0 474 246 A2	06/09/91	EP				
EC	0 654 733 A1	05/07/94	EP				


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EC	The Visual Instruction Set (VIS) in UltraSPAR TM , L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, IEEE, May 3, 1995, 462-469
EC	Osborne McGraw-Hill, i860 TM Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn, 1990, 8-10; 171-175; 182-183
EC	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998, 5-26
EC	Ruby B. Lee, "Accelerating Multimedia with Enhanced Microprocessors", IEEE Micro, April 1995, 22-32.

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION <div style="display: flex; align-items: center; justify-content: center;">  <div style="margin-left: 20px;">(PTO-1449)</div> </div>				ATTY. DOCKET NO. 43876-155		SERIAL NO. 10/757516 Continuation of Serial No. 10/646,787	
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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
EC	4,025,772	05/24/77	Constant				
	4,489,393	12/18/84	Kawahara, et al.				
	4,701,875	10/20/87	Konishi, et al.				
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	5,157,388	10/20/92	Kohn				
	5,201,056	04/06/93	Daniel, et al.				
	5,268,855	12/07/93	Mason, et al.				
CC	5,268,995	12/07/93	Diefendorff, et al.				
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EC	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987						
EC	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen, IEEE Micro. April 1992, 40-63						
EC	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.						
CC	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, Microprocessor Report July 31, 1995, 15-16						
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Eui G.L.			2/3/09				

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EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (prev)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
CC		US 5,819,101	10/6/1998	Alexander Peleg, et al	
		US 5,881,275	3/9/1999	Alexander Peleg, et al	
		US 6,119,216	9/12/2000	Alexander Peleg, et al	
		US 6,516,406	2/4/2003	Alexander Peleg, et al	
		US 6,539,467	3/25/2003	Timothy D. Anderson, et al	
		US 6,574,724	6/3/2003	David Hoyle, et al	
CC		US 6,631,389 B2	10/7/2003	Derrick Chu Lin, et al	
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		US			

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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No

OTHER ARTS AND LITERATURE

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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† Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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EC		US 4,785,393	11/15/1988	Chu et al.	
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		US 5,031,135	07/09/1991	Fateh	
		US 5,260,598	01/19/94	Osadi et al.	
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		US 5,740,093	04/14/1998	Sharangpand	
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		US 5,899,849	04/27/1999	Tan	
		US 5,996,057	11/00/1999	Hunter L. Scales, III, et al.	
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						Yes No

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
EC		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)
		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.29 IEEE P1596.4-199X (May 1995)
		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).
		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).
CC		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990).

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